

TARGET PARTICIPANTS

- ★ Faculty members of Engg. Colleges / Universities
- ★ Research scholars & PG/UG students pursuing research / project work in this field
- ★ Academicians and R&D personnel from industries
- ★ Restricted to max. of 40 on first-come-first-serve

REGISTRATION DETAILS

- ★ Those wishing to attend this workshop should register online at tinyurl.com/WCTVTD2019-regform
- ★ Due to limited seats, registration will be on a first-come-first-served basis. Confirmation of registration will be sent by email. Enrollment will be strictly online.
- ★ Registration fee is Rs. 750/-. It can be paid through online or Demand Draft drawn in favour of "ECE Association" payable at Chennai. For online payment, the bank details are:
- ★ Bank Name: City Union Bank, Branch Code: 117, IFSC: CIUB0000117,
- ★ Account Number: 11700 10008 84762,
- ★ Account Name: ECE Association

REGISTRATION FEE INCLUDES

- ★ Lunch and Refreshment
- ★ Conference Kit
- ★ Accommodation can be arranged on request on sharing basis for outstation participants in University Hostels. Charges are Rs. 500/- per day, inclusive of breakfast and dinner.
- ★ Note: No TA / DA provided

PATRONS

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ADDRESS FOR COMMUNICATION

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Workshop on Current Trends in VLSI Testing and Design

12-13, July 2019

Organized by
**Department of
Electronics and Communication Engineering**



Faculty of Engineering & Technology

SRM IST, Kattankulathur – 603 203
Kancheepuram District, Tamilnadu.
Phone: 044-27452270, 27453380.

Website : www.srmuniv.ac.in

In Association with



Important Dates

Register by	: Jun 25, 2019
Intimation of selection	: Jul 05, 2019
Date of Workshop	: 12-13, July 2019

ABOUT THE INSTITUTE

SRM Institute of Science and Technology (formerly known as SRM University) is one of the top ranked universities in India with over 50,000 students and 3,177 faculty members, offering a wide range of Undergraduate, Postgraduate and Doctoral Programs in Engineering, Medicine & Health Sciences, Management, Science & Humanities, Law, and Agricultural Sciences. Over the last three decades, it has set standards in experiential learning and knowledge creation across various fields. It has reached beyond borders to universities and corporate across India and around the World. There are four sprawling campuses - three in and around Chennai, and one near New Delhi - spread across 250 acres with all facilities. It had conducted 98th Indian Science Congress in January, 2011 which was attended by 7,000 delegates from India and abroad, including six Nobel Laureates and several eminent scientists. SRM IST is the first private university in India to launch a nano-satellite, named SRMSAT, into space, on board the PSLV-C18 from Sriharikota on October 12, 2011.

SRM IST is accredited by NAAC with the highest Grade of 'A++'. QS, the world renowned international ranking agency, has rated SRMIST as 'Four Star, Institute and QS-IGAUGE has awarded Diamond Rating to SRMIST. SRMIST is classified as Category I University by UGC/MHRD and enjoys 12B status too under UGC Act.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering (ECE) was established in 1991 under the affiliation of University of Madras. It offers one UG Program, three PG programs and Doctoral Program. It has 18 well equipped labs. It has ample resources in computing equipment, including personal computers running the full suite of CADENCE, OrCAD, MATLAB and Network Simulation softwares. It has USRP-RIO SDR

hardware, Vector Signal Generator & Analyzer, Network Analyzer and ADS & HFSS tools.

The department has strong collaboration with industries such as Central Electronics Engineering Research Institute (CEERI) Chennai, Nuvoton Technology Corporation, Taiwan, National Instruments and ALTERA. It received research grants from Department of Science and Technology (DST) under Funds for Improvement of Science and Technology (FIST), in the area of MIMO-OFDM. Recently, it has received research grants from Defense Research and Development Organization (DRDO), Board of Research in Nuclear Science (BRNS) and Indian Space Research Organization (ISRO). The B.Tech (ECE) program of Kattankulathur campus of SRMIST is accredited by EAC of ABET. www.abet.org

ABOUT THE WORKSHOP

Integrated circuits are used in virtually all electronic equipments today and have revolutionized the world of electronics. Computers, mobile phones, and other digital home appliances are now inextricable parts of the structure of modern societies, made possible by the low cost of production of VLSI circuits. This rapidly evolving sector offers exciting opportunities in verification based jobs for those with strong fundamentals in electronic circuit design and hardware description languages. The skill to put know-how of VLSI concepts to practice has churned the interest in VLSI design and verification fields. The purpose of this WORKSHOP is to design, develop and re-engineer integrated circuits (ICs), which will kindle the interest of participants towards this pervasive and primordial technology.

Theme of the Workshop

This Workshop aims to enhance technical and professional competency of the faculty members, UG/PG Students and PhD Scholars in the domain of VLSI Design and Testing. The main objective of this workshop is to

provide theoretical and practical knowledge on recent trends in VLSI circuit design and testing. During this program the academicians and industry personnel in their respective areas of expertise will discuss the current scenario advancements in this field. In depth knowledge of upcoming trends in hardware and software co-design will also be delivered by experts. Particular emphasis would be given to the demonstration of advanced hardware, EDA tools and hands-on-sessions for VLSI design and testing would be part of the program. The course would also provide opportunity to the young faculty/researchers/Students to interact across subfields and gain broader exposure to the major themes of this field.

RESOURCE PERSONS

Invited lectures are delivered by experts on the basic aspects and recent advancements in the area of VLSI Physical Design, Low Power Verification, Artificial Intelligence Accelerator and FPGA Implementation and also hands on training by industry experts using Cadence and Vivado.

1.Dr. Prasanna Kumar Misra, Assistant Professor, Department of Electronics and Communication Engineering, IIIT Allahabad.

2.Mr. Hiram Prasanna, Director, Altran, Chennai.

3.Ms. Anitha Stalin, Senior Project Manager, Altran, Chennai.

4.Dr. S. Balaji, Associate Professor, LICET, Chennai.

5.Mr. Ibrahim Hussain, Design Verification Engineer, Altran, Bangalore.

6.Mr. Sabarigirivasan, Manager, Altran, Chennai.

7.Mr. R. Ramesh, Lead Engineer, Altran, Chennai.