

Reference No.: DST_SERB/CRG/2020/ 004073/JRF Call

Date: 25/03/2021


Applications are invited from Indian citizens for a temporary post of Junior Research Fellow (JRF) for the DST- SERB sponsored project in the Department of Electrical and Electronics Engineering, SRM Institute of Science and Technology, Kattankulathur, Chennai.

Title of the Project	Development of Indigenous Cost Effective Retrofit Quasi Dynamic High-Performance Wireless Power Transfer Charging modules for Slow Moving Roadway-Powered Electric Vehicles
Sponsoring Agency	DST-SERB-CRG
Project Sanction No.	CRG/2020/004073
No. of Post	01- JRF
Duration	3 Years
Principle Investigator	Dr. Narayanamoorthi R, Assistant Professor, Department of EEE, SRM Institute of Science and Technology
Co-Investigator	Dr. R. Sridhar, Associate Professor, Department of EEE, SRM IST

1	Age Limit	35 Years
2	Essential Qualification	M.Tech/ M.E in Electrical Engineering (Specialization in Power Electronics). Candidates with GATE score preferred.
3	Desirable Experience	Candidates should have good knowledge in Power Electronics and hands on experience in developing and working on hardware prototype of the power electronic converters, PCB design software and basic knowledge in Microcontroller/ FPGA programming
4	Salary	Rs. 35, 960/- per month for first 2 years + Rs. 40, 600/- per month for third year. (Including HRA)
5	Relaxation on qualification and Age	SC/ST/OBC/WOMEN/ PWD candidates will get relaxation as per Govt of India Rules.
6	Last date of receiving/ submission of filled application	15/04/2021
7	Reporting	Applicants are requested to submit their filled up application along with C.V to the PI accompanying photocopies of all the documents. Applicants are also requested to send the soft copy of the duly filled applications along with CV & all necessary documents to the PI through email only (narayanamoorthi.r@gmail.com). Call letter for the shortlisted candidates will be sent via email.
8	Probable date of interview, time and venue	20/04/2021, 10 A.M, Department of Electrical and Electronics Engineering
9	No objection certificate	Employed candidates are to submit no objection certificate from his/ her employer at the time of interview.
10	Institute right	<ul style="list-style-type: none"> Selected JRF should register for Ph.D(Full Time) at SRMIST. The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be recruited if suitable candidates are not found.
11	Signing of Agreement	Selected Candidates shall have to sign an agreement at the time of joining.
12	Date of Joining	Immediate
13	Selection Process	<ul style="list-style-type: none"> Certificate Verification Written Test- 45 minutes Personal Interview

Copy to:

DST-SERB-CRG
 Dean CET, Director Research, Dean Research
 HoD EE, SRM IST



Dr. Narayanamoorthi R
 Principal Investigator

SRM Institute of Science and Technology, Kattankulathur

Format of Application
for **Junior Research Fellow**

Paste recent
passport size
colored
photograph

1. Reference No: Dated:
2. Title of the Project:
3. Department:
4. Name of the Applicant (In Block Letters):
5. Father's Name/ Husband's Name:
6. Date of Birth (DD-MM-YYYY):
7. Age as on 24-03-2021:
8. Postal Address:

Email id:

Phone No:

9. Permanent Address:

10. Nationality:
11. Marital Status:
12. Whether Belongs to SC/ST/PH/OBC (If yes, attach Certificate):
13. Educational Qualification: (Starting from Class X)

Exam Passed	Board/ University	Division	% of Marks obtained/ CGPA	Year of Passing	Subjects

14. GATE Examinations Passed:

Subject	Qualifying date	Valid Upto

15. Details of Employment:

Sl. No.	Name of the Organization	Name of the Post	Period	Scale of Pay and Present Pay	Nature of Duty

16. Publication Details:

17. Any other relevant information that you may like to furnish:

Declaration

I declare that the above information are true and correct to the best of my knowledge and belief.

Date:
Place:

Signature of the applicant