ACADEMIC CURRICULA

UNDERGRADUATE INTEGRATED POST GRADUATE DEGREE PROGRAMMES

(With exit option of Diploma)

(Choice Based Flexible Credit System)

Regulations 2021

Volume - 1

(Revised on July 2024)



SRM INSTITUTE OF SCIENCE AND TECHNOLOGY

(Deemed to be University u/s 3 of UGC Act, 1956)
Kattankulathur, Chengalpattu District 603203,
Tamil Nadu, India



SRM INSTITUTE OF SCIENCE AND TECHNOLOGY Kattankulathur, Chengalpattu District 603203, Tamil Nadu, India

35. B.Tech. in Electronics Engineering (VLSI Design and Technology)

35. (a) Mission of the Department

· /	1
iviission Simi – T	Build an educational process that is well suited to local needs as well as satisfies the national and international accreditation requirements.
	Attract the qualified professionals and retain them by building an environment that fosters work freedom and empowerment.
Mission Stmt – 3	With the right talent pool, create knowledge and disseminate, get involved in collaborative research with reputed universities and produce competent graduands.

35. (b) Program Educational Objectives (PEO)

PEO – 1	Apply the acquired knowledge and skills in solving real-world engineering problems, considering national/global and societal issues such as health, environment, and safety.
PEO – 2	Design VLSI systems, which are economically feasible and socially relevant for promoting sustainable semiconductor and electronics eco-system.
PEO – 3	Develop an attitude toward pursuing knowledge and advanced education for sustained career advancement to adapt to emerging fields.
PEO – 4	Demonstrate leadership qualities and effective communication skills to work in a team of enterprising people in a multidisciplinary and multicultural environment with strong adherence to professional ethics.

35. (c) Mission of the Department to Program Educational Objectives (PEO) Mapping

		Mission Stm	t 1	Mission Stmt 2	Mission Stmt 3					
PEO - 1		1	The Co	2		3				
PEO - 2	1	3	100	3	Ye. 1	3				
PEO - 3	1	2	100	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	All at	3				
PEO - 4		3	N WARRY	3	75,000	3				

^{3 –} High Correlation, 2 – Medium Correlation, 1 – Low Correlation

35. (d) Mapping Program Educational Objectives (PEO) to Program Outcomes (PO)

			17,0	Z	Pro	gram Ou	tcomes (PO)		200	ئىيا د.		Prog	eci <mark>fic</mark>	
	1	2	3	4	5	6	7	8	9	_ 10	11	12	Outo	comes (P	SO)
	Engineering Knowledge	Problem Analysis	Design/development of solutions	Conduct investigations of complex problems	Modern Tool Usage	The engineer and society	Environment & Sustainability	Ethics	Individual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	PS0-1	2-0Sd	PSO-3
PEO - 1	3	3	7-1-	1	R	3	3	2		-			3	3	-
PEO - 2		- /	3	3	3	3	4-1	1	2	-	3	1	3	-/	-
PEO - 3		-		3	3	-	2	2	-	2		- 3	-	7 -	3
PEO - 4	-	-	-	-	-	-	-	3	3	3	3	/	* - /	-	3

^{3 –} High Correlation, 2 – Medium Correlation, 1 – Low Correlation

PSO – Program Specific Outcomes (PSO)

PSO - 1 des PSO - 2 Pro Des	Problem Solving Skills: Contribute to the Indian/global semiconductor and electronics ecosystem with innovative approaches to
F30 - 1	design, manufacture, and test integrated systems.
DCO 2	Professional Skills: Apply knowledge of complete design flow from specification to silicon in areas of both digital and analog VLSI
P30 - 2	Design
DCO 3	Successful Career and Entrepreneurship: Promote inter-disciplinary work in semiconductor physics, computer science, and
P30 - 3	electrical engineering to create exciting new systems with greatly increased functionalities.

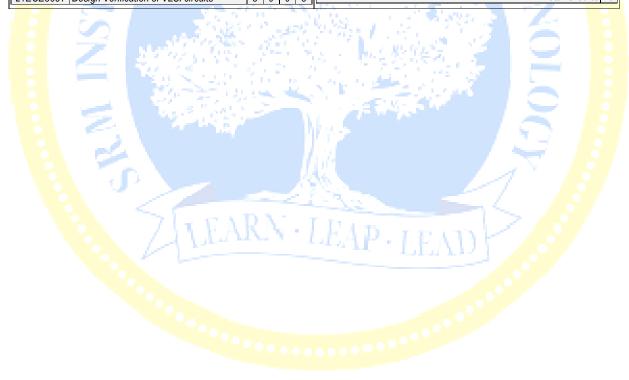
35. (e) Program Structure: B.Tech. in Electronics Engineering (VLSI Design and Technology)

()	Humanities & Social Sciences						Basic Science Courses (B)	<i>J /</i>			
	including Management Courses (H)					Course	Course	Н	ours	:/	
Course	Course		ours			Course	Title	٧	Veek		
		V	Vee					L	T	Р	С
		L	Τ	Р	С		Physics: Electromagnetic Theory,	3	1	2	5
		2	1	0	3		Quantum Mechanics, Waves and Optics		Ť		
							Chemistry	3	1	2	5
							Calculus and Linear Algebra	3	1	0	4
	German						Advanced Calculus and Complex Analysis	3	1	0	4
		2	1	0	3		Transforms and Boundary Value Problems	3	1	0	4
							Probability and Stochastic Processes	3	1	0	4
							Discrete Mathematics	3	1	0	4
						21BTB103T		2	0	0	2
		1	0	2	2		To	tal	Crec	dits	32
		2	0	0	2						
21GNH401T Behavioral Psychology			1	0	3	Mizzy					
	otal	Cre	dits	13							
Humanities & Social Sciences including Management Courses (H)							Open Elective Courses (O)				
Course		Hou				(Any 3 courses)					
			We			Course	Course		ours		
		L	. T	Р	С	Code	Title	V	Veek		
			0 (2			L	T	Р	С
		C		4	2		Short Range Wireless Communication	3	0	0	_
		3	3 1	0	4	21ECO102J	Electronic Circuits and Systems	2	0	2	3
			3 0	2	4	21ECO103T	Modern Wireless Communication	3	0	0	3
	Computer Organization and Architecture	9 3	3 1	0	4		Systems	Ť	Ť		Ť
21DCS201P 1 Design Thinking and Methodology			0	4	3		PCB Design and Manufacturing	2	0	2	3
21CSS303T Data Science			2 0	0	2		Fiber Optics and Optoelectronics	3	0	0	_
	otal	Cre	dits	21	21ECO106J	Embedded System Design using Arduino	2	0	2	3	
					21ECO107J	Embedded System Design using Raspberry Pi	2	0	2	3	
Cauraa	Cauras	Н	ours	s /		21ECO1081	3D Printing Hardware and Software	2	0	2	3
		٧	Vee	k			5G Technology – An Overview	3	0	0	3
Code	riue	L	Τ	Р	С	212001091		_	Crec		_
21ECC101J	Electronic System and PCB Design	2	0	2	3	4-		lai	OTEC	anto	03
		3	0	0	3		Non Credit Courses (M)				_
21ECC202T	Analog and Linear Electronic Circuits	3	0	0	3	Course	Course		Hour		
21ECC203T	Digital Logic Design	3	0	0	3	Code	Title	_	Wee		4 .
		3	0	0	3			Į L	Τ	Р	
21ECC205T	Electromagnetic Theory and Interference	3	0	0	3		Professional Skills and Practices	0		2	
21ECC211L 1	Devices and Digital IC Laboratory	0	0	4	2	21PDM102L		0			
215002221 1	Analog and Linear Electronic Circuits	0	0	4	2		1 Verbal Reasoning	0		2	0
		U	U	4	2	21PDM202L	Critical and Creative Thinking Skills	0		2	
		3	1	0	4	21PDM301L	¹ Analytical and Logical Thinking Skills	0		2	4
		J		U	7		1 Employability Skills and Practices	0		2	_
		3	0	0	3	21CYM101T		1		+	+-
	0,	3	0	0	3	21LEM101T		1		0	_
		3	0	0	3	21LEM102T		1	0	0	_
						21LEM201T		1	0	0	0
		0	0	4	2	044 =	Universal Human Values-II:				
711-1.1.3331 1	ŭ ŭ	0	0	4	2	21LEM202T	¹ Understanding Harmony and Ethical Human Conduct	2	1	0	3
	Laboratory RF Integrated Circuits and systems	3	0	0	3	21LEM301T		1	0	0	0
	Physical Design and Automation	2	0	0	3	21LEM302T		1		0	
		2	1	0	3		Physical and Mental Health using Yoga	+	1	U	U
210302001	Artificial Intelligence		Cro	•	_	21GNM101L					
	10	otal	ore	uitS	4ŏ	21GNM102L 21GNM103L		0	0	2	0
Project Wor	k, Seminar, Internship in Industry / High	ner T	Tech	nnic	al	21GNM103L 21GNM104L					
	Institutions (P)					Z I GIVIVI IU4L		atal	Cred	dito	02
							10	riai	OIG	นเเอ	U

Course Code	Course Title		lours Neel	-							
Code	Title	L	Т	Р	С						
	Community Connect	0	0	2	1						
21ECP302L 1		0	0	6	3						
21ECP303T 1	MOOC	3	0	0	J						
21ECP401L	Major Project	0	0	30	15						
21ECP402L	Major Project	0	0	20	10						
21ECP403L	Internship#	0	0	10	5						
	Total Crod										

	Professional Elective Courses (E) (Any 6 Courses)											
Course Code	Course Title		Hours / Week									
	1.00	L	Т	Р	С							
21ECE201J	Python and Scientific Python	2	0	2	3							
21ECE260T	3	0	0	3								
21ECE261T	Measurements and Instrumentation	3	0	0	3							
21ECE262T	Low Power Sensors Technology	3	0	0	3							
21ECE263T	Micro, Nano Electromechanical Devices	3	0	0	3							
21ECE204T	Optoelectronics	3	0	0	3							
21ECE205T	Flexible Electronics	3	0	0	3							
21ECE301T	Nanoscale Electronic Devices	3	0	0	3							
21ECE361T	Consumer Electronics and Trouble Shooting	3	0	0	3							
21ECE362T	Quality and Reliability Engineering	3	0	0	3							
21ECE363T	Electronic Packaging	3	0	0	3							
21ECE364T	Digital Signal Processors Architectures											
21ECE365T												

		Professional Elective Courses (E)								
	Course	Course Title		Hours / Week						
l	Code	riue	L	Т	Р	С				
	21ECE460T	Emerging Processor based System Design	3	0	0	3				
1	21ECE461T	3	0	0	3					
1	21ECE462T	Intelligence for Electronics Design								
1	21ECE463T	Scripting Language for Electronic Design Automation	3	0	0	3				
1	21ECE464T	Statistical Analysis and Optimization for VLSI	3	0	0	3				
	21ECE465T	Device and Process Modelling	3	0	0	3				
١	21ECE466T	Low Power Circuit Design	3	0	0	3				
1	21ECE467T	High speed IC Design	3	0	0	3				
1	21ECE468T	System and Network on Chip	3	0	0	3				
١	21ECE404T	Terahertz Devices and Applications	3	0	0	3				
1		To	tal	Cre	dits	18				



35. (f) Programme Articulation: B.Tech. in Electronics Engineering (VLSI Design and Technology)

35. (f) Pro	ogramme Articulation: B.Tech.	ın El	ectr	onic		_		_			gn a	na I	ech	11010		
		1	2	3	<u>F</u>	rogra 5	m Ou	tcome 7	8 (PO	9	10	11	12	1	PSO 2	3
Course Code	Course Name	Engineering Knowledge	Problem Analysis	Design/development of solutions	Jo	Modern Tool Usage	The engineer and society	Environment & Sustainability	Ethics	ndividual & Team Work	Communication	Project Mgt. & Finance	Life Long Learning	PSO-1	PSO-2	PSO-3
						Mo	Ţ	Ēņ	臣	lud	රි	Prc	Ţ		S	PS
	Digital Logic Synthesis using HDL	2.66	2.4	2	3	-	-	-	-	-	-	-	-	2.8		-
	CMOS Analog and Mixed Signal IC Design	1	2	3	2.5	11-11	2	-	-	-	-	-	2	-	-	-
	RF Integrated Circuits and Systems	2	2	3	-	-	-		٠÷.	-	-	-	-	- 2.75	-	3
	Physical Design and Automation	2.5	2.5	- 3	2	3	-	-		+,	-	-	2	2.75	2	2
	CMOS Analog and Digital VLSI Laboratory Industrial Electronics	2.75	2.25	2	3	J -	-					-		1.66	2	-
	Measurements and Instrumentation	3	2	2	2		7.	-	-	-		-	2	1.00	-	-
	Low Power Sensors Technology	2.2	-	3	-		, T	4	Ä.				_	2.66		
	Consumer Electronics and Troubleshooting	2.75	2	2	3	_	-	4		_	-			1.66	2.5	_
	Digital Signal Processors, Architectures and								-	m						<u> </u>
	Applications	2.2	-	3	-	-	-	1	- 1	1	A,	-	"	2	2	ı -
	Electronic Packaging	3	2	7.4		-	-	-	-	-	7	-	2.33	2.25	_	-
	Design Verification of VLSI Circuits	2.75	2.25	1.5	7-1	2	-	-	-	'	٠.,	P.	-	3	\	-
21ECE460T	Emerging Processor based System Design	4	2	2.4	1.66	1.5	-	-	-		Ţ		-		2	2
	Semiconductor Memory Design	2.8	2	4		P.	-	-	-	•		-	-	1.8		-
21ECE4621	Machine Learning and Artificial Intelligence for Electronics Design	1.5	2	2	3	3	 	-			k	1	2.5	3	3	ŀ
216664031	Scripting Language for Electronic Design Automation		2	3	2.66	2	-	7				-7	9	1	- 1	2
216064041	Statistical Analysis and Optimization for VLSI	1	2	3	2.5	d			57 g	127		-	Ź	1	-	1.8
	Low Power Circuit Design	3	3	2.8	1.66	3	30	16.7		-	-	-	-	3		-
	High speed IC Design	2.2	2.8	127		-		1.5	72.	-1	-	-	-	2	2	
	System and Network on Chip	111	2.5	3	2	2		-	174	- 14	-	-		3	-	2
	Electronic System and PCB Design	3	2.5	2.67	Ŀ	3	-		-		-	2	-	2.8	2.5	
	Solid State Devices	3	2	-		-	135	-1		-	-	-	1	1	-	
	Digital logic Design	3	2	2	-4	3		-	-	-	-	- 1	-	3	- 1	-
	Electromagnetic Theory and Interference Devices and Digital IC Laboratory	2.4	2.6	Ŋř.		1	-	-	-	-	-	-4	-3	1		-
	Analog and Linear Electronic Circuits	2	2	3	-	-	-	-	-	-		-	-			3
	Signal Processing	2	2.2	3	3	-	-	-	-	-	-	- 7	Y .	-	=	2.2
21FCC2221	Analog and Linear Electronic Circuits	2	-	2	- -	3	_	_	-			7	_			- 2.2
	Laboratory Microprocessor, Microcontroller, and		•		34						\prec			0.07		
ZIECC30IP	Interfacing Techniques VLSI Design and Technology	Ľ,	- 3 - 2.4	2.25	44	3	-			1-1	Ŀ	7		2.67	2	<u>-</u>
	VLSI Design Laboratory	3	3	2.20	-	1		-	πħ	Ð				1		-
	Micro, Nano Electromechanical devices	2.4	2	2.75			_	-			٠.	-		3	2.67	3
	Optoelectronics	2.8	2.67	2.67	2.67	-	-	_	_	-				-	-	2.4
	Flexible Electronics	3	3	-	-	-	-	-	-			_	3	-	-	
	Nanoscale Electronic Devices	3	2.5	-	-	2.5	-	-	-		T.		-	2	-	2.5
	Quality and Reliability Engineering	3	1.5	2	-	-	-				-	-	-		-	2
	Design Verification of VLSI circuits	2.75			-	2	-	0	-	-				3		
21ECE460T	Emerging Processor based System Design	9 45	2	2.4	1.66	1.5		-		-	-	-	-		2	2
	Semiconductor Memory Design	2.8	2	-	-	-	-		-	-	-	-	-	1.8	-	
	Device and Process Modelling	3	3	-	-	-	-		-	-	-	-	3	3		-
	Terahertz Devices and Applications	3	2.75	2	2	-	-	2	-	-	-	-	-	2.3	2	3
	Python and Scientific Python	-	2.67	3	2	3	-	-	-	3	-	-	-	3		2.67
	Community Connect						3		3	3	2		_			H
21ECP302L		3	3	3	3	3	3	3	3	3	3	3	3	3	3	3
21ECP303T		3	2	_	_	_	_	_	_	_	_	_	_	3		_
	Major Project	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3
	Major Project	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3
21ECP403L	Internship	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3
	Program Average	2.53	2.30	2.51	2.37	2.32	2.00	2.00	-	3.00	-	2.00	2.23	2.28	2.21	2.37

35. (g) Implementation Plan: B.Tech. in Electronics Engineering (VLSI Design and Technology)

(g) IIII	Semester – I						Semester – II		,, ,		=
					H	lour	s/	П			
Course Course Code Title			ours Vee			Course	Course		Nee		
Code	Title	ΤÌ	T	P	С	Code	Title	T	ΙΤ	Р	С
21LEH101T	Communicative English	2	1	0	3	21LEH102T	Chinese				Ħ
	Calculus and Linear Algebra	3	1	0	4	21LEH103T	French				
	Physics: Electromagnetic Theory,	3	1	2	5	21LEH104T	German				
ZIPYBIUIJ	Quantum Mechanics, Waves and Optics	J	-	2	b	21LEH105T	Japanese	2	1	0	3
	Engineering Graphics and Design	0	0	4	2	21LEH106T	Korean				
	Electrical and Electronics Engineering	3	1	0	4	21LEH107T	Spanish				
	Environmental Science	1	0	0	0	21LEH108T	Russian				
	Professional Skills and Practices	0	0	2	0	21GNH101J		1	0	2	2
21LEM101T ¹	Constitution of India	1	0	0	0	21MAB102T	Advanced Calculus and Complex	3	1	0	4
	To	otal (Cre	dits	18		Analysis	Į,			<u> </u>
	Semester - III					21CYB101J		3	1	2	5
_		I	lou	rs /		21ECC101J		2	0	2	3
Course	Course		We			21CSS101J	Programming for Problem Solving	3	0	2	4
Code	Title	L	Т	Р	С	21BTB103T	Biology	2	0	0	2
21MAB201T	Transforms and Boundary Value Problems	3	1		4		Basic Civil and Mechanical Workshop	0	0	4	2
	Social Engineering	2	_	0	2		General Aptitude	0	0	2	0
	Computer Organization and Architecture	3		0	4		Physical and Mental Health using Yoga				
21ECC201T 2	Solid State Devices	3		0	3		National Service Scheme	0	0	2	0
	Digital Logic Design	3			3		National Cadet Corps				
21ECC205T Electromagnetic Theory and Interference		3	_		3	21GNM104L	National Sports Organization	0651	C	al:4 -	25
	Devices and Digital IC Laboratory	0			2			otal	∪re	aits	25
	Professional Ethics	1	0	-	0		Semester – IV				
	Verbal Reasoning	0	0	2	0	Course	Course		lours		
	Universal Human Values-II: Understanding	9 2	1	0	3	Code	Title	_\	Nee		↓.
ETELINIZOZI	Harmony and Ethical Human Conduct			Ŭ				L	Τ	Р	С
	Т	otal	Cre	dits	24		Probability and Stochastic Processes	3	1	0	4
	Semester – V						Analog and Linear Electronic Circuits	3	0	0	3
Course	Course	H	ours	s/			Signal Processing	3	0	0	3
Course Code	Course Title		Vee	k]		Analog and Linear Electronic Circuits	0	0	4	2
		L	T		С		Laboratory	0	4	_	
	Discrete Mathematics	3	1	0	4		Artificial Intelligence	2	7	0	3
	Microprocessor, Microcontroller, and	3	1	0	4		Professional Elective-I	1	0	1	3
	Interfacing Techniques						Design Thinking and Methodology Critical and Creative Thinking Skills	0	0	2	0
	VLSI Design and Technology	3	0	0	3	Z I F DIVIZUZ I		otal	-		-
	VLSI Design Laboratory	0	0	4	2			Jidi	J. C	uito	121
	Professional Elective – II				3	I	Semester – VI	1	1	- /	T
_	Open Elective – I Community Connect	0	0	2	3	Course	Course		lour		
	Analytical and Logical Thinking Skills	0	0	2	0	Code	Title	-	Иее ТТ	K P	1
		1	_		_	21CSS303T	Data Science	L	_	_	C
ZILEWIJUII'	Indian Art Form	otal (0 dits		21ECC305T 2		3	0	0	3
		otal (ore	นเเร	20		CMOS Analog and Mixed Signal IC			U	
	1 mar					21ECC306T	Design	3	0	0	3
						0.4555	CMOS Analog and Digital VI SI				t
	Semester – VII					21ECC333L ¹	Laboratory	0	0	4	2
_		Н	ours	s /		Е	Professional Elective – III				3
Course	Course		Vee			E	Professional Elective – IV				3
Code	Title	Ĺ	T	Р	С	0	Open Elective – II				3
21GNH401T	Behavioral Psychology	2	1	0	3	21ECP302L ¹		0	0	6	
21ECC403T ²		3	0	0	3	21ECP303T 1		3	0	0	3
21ECC404T	Physical Design and Automation	3	0	0	3	21PDM302L 1	Employability Skills and Practices	0	0	2	0
Е	Professional Elective – V				3	21LEM302T 1		1	0	0	0
E	Professional Elective – VI				3			otal	Cre	dits	22
0	Open Elective – III				3		Semester - VIII				
		otal	Cre	dits	18			Нο	urs	,	
				Course	Course		iui s leek				
#Students have	e to register either 21ECP401L or 21ECP4		Code	Title	1 .		P	С			
	oth in eighth semester	_				21ECP401L	Major Project	0		30	15
						21ECP401L		_	_	20	10
						21ECP403L				10	5
						21201 4002		otal (-	-
		<u> </u>	10		J. 00						
		<u> </u>									



SRM INSTITUTE OF SCIENCE AND TECHNOLOGY

(Deemed to be University u/s 3 of UGC Act, 1956)

Kattankulathur, Chengalpattu District 603203, Tamil Nadu, India